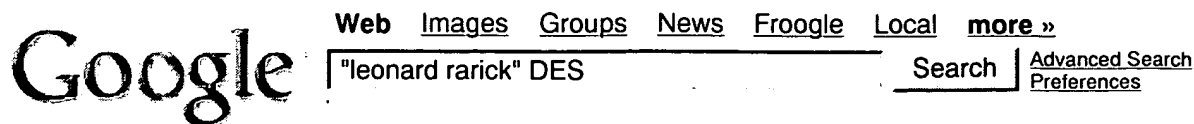


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L1	6704	(microprocessor or processor) with ("DES" or "Data encryption standard")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 18:53
L2	357	I1 with instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 18:54
L3	16	I2 and ("GPRs" or "general purpose regster")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 18:50
L4	182	(microprocessor or processor) with "Data encryption standard"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 18:53
L5	17	I4 with instruction	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 20:19
L6	14	"6088800"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 20:33
L7	2	I1 and I6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/10 20:47
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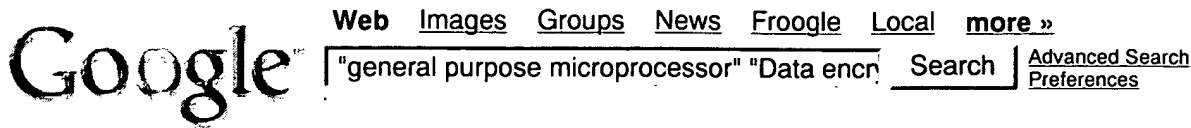
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